



Logic Synthesis

Srinivas Devadas, Abhijit Ghosh, Kurt Keutzer

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Logic synthesis enables VLSI designers to rapidly lay out the millions of transistors and interconnecting wires that form the circuitry on modern chips, without having to plot each individual logic circuit. This guide to logic synthesis techniques spotlights not only the synthesis of two-level, multi-level and combinational circuits, but also their testability.

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